


<div>Search Notes</div> <div></div>	Application/Control No.	Applicant(s)/Patent under Reexamination	
	10/710,882	HALL ET AL.	
	Examiner	Art Unit	
	Andrew Lai	2616	

SEARCHED			
Class	Subclass	Date	Examiner
370	474, 503	1/7/2008	AL

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES (INCLUDING SEARCH STRATEGY)		
	DATE	EXMR
1: downhole near network	1/7/2008	AL
2: (network near (controller processor (process\$3 near unit))) with (assembler disassembler)	1/7/2008	AL
3: 1 & (clock near (set\$3 synchroni\$6))	1/7/2008	AL
4: (((peripheral external) near (device clock)) with control\$3) and network and (packet near (assembl\$3 disassmebl\$3))	1/7/2008	AL
5: (clock near generator) with (oscillator transistor crystal ((RC LC LRC) near circuit))	1/8/2008	AL
6: 5 & (packet near network)	1/8/2008	AL
7: (clock near synchronization) with GPS	1/9/2008	AL
Discussion with SPE Kwang Yao on claim interpretation	1/7/2008	AL